




**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

**FACULTY PROFILE**

<b>Personal Details</b>	Position: ASSISTANT PROFESSOR	Dept: Electrical & Electronics Engineering			
	Name: <b>V NAGA SIVA RAMA MURTHY</b>				
	Father Name: VHS AVADHANI	Mother Name: VS.LAKSHMI			
	Date of Birth: 15 <sup>TH</sup> AUGUST 1986	Age:31Years	Gender: MALE		
	Address: D. No:32-173/1	Street: VASAVI NAGAR			
	Location:OPP.SNKM THEATRE	City/Village: ADDANKI			
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<b>Academic Details</b>	Qualifications	University Name	Institute Name	Year of Passing	Regular/ Distance/ Part Time	Specialization
	SSC	B.S.E	VBR, ADDANKI	2001	Regular	General
	Inter/Diploma	B.I.E	UMRJC, ONGOLE	2003	Regular	MPC
	UG: B.Tech	JNTUH	MeRITS, UDAYAGIRI	2007	Regular	EEE
	PG: M.Tech	JNTUK	NEC, NARASARAOPET	2011	Regular	P&ID
	Ph.D: (Pursuing)	KLU, VIJAYAWADA	KLU, VIJAYAWADA	----	Part Time	POWER ELECTRONICS

Experience Total: 09	Teaching: 7	Industry: 02	Research: 03	Total: 09
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<b>Number of Publications :</b>	National Journal:02	International Journal:02	International Conferences:02
<b>Memberships:</b>	IAENG,IRED		

**Disclaimer**

I certify that my answers are true and complete to the best of my knowledge.