



RAMACHANDRA COLLEGE OF ENGINEERING

(Approved by AICTE, New Delhi, Affiliated to JNTUK: Kakinada)
NH-5 Bypass Road, Vatluru (V), ELURU – 534 007, A. P.

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Workshop Report

Organized by : Ramachandra College of Engineering
Name of the Speaker : Dr. K. Durgaprasad
Designation : Director, HR services, ERDL, Bangalore
Topic : Two Day workshop on P-Spice
Venue : Auditorium
Date & Time : 21.07.2016 to 22.07.2016 & 10.00 to 04.40
Conducted for :

Branch	Year	Semester	No of Students Attended
ECE	IV	I	75
ECE	III	I	130
ECE	II	I	95
Total No of Students Attended			213

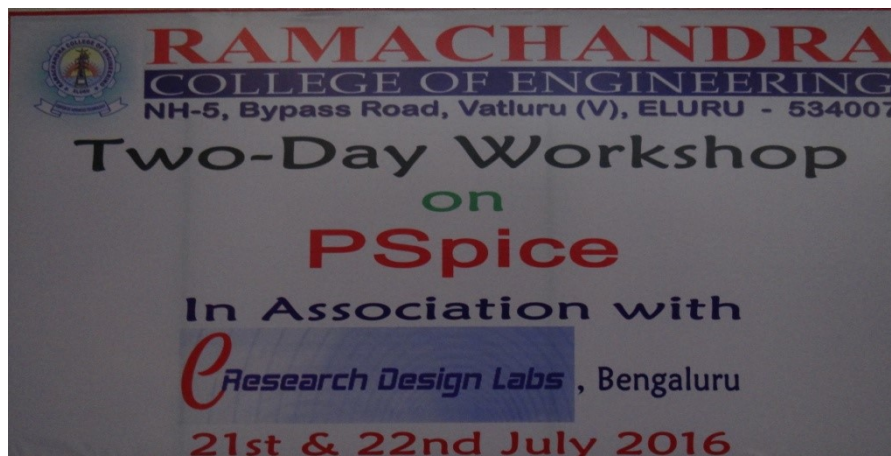
Profile of the Speaker

ERDL [Electronics Research Design Labs] is a Bangalore based non-government body, which came in to existence in the year 2005. ERDL maintained by Doctorates in the field of electronics, has been established to encourage innovation in engineering design for the Indian context. It is a platform that facilitates young and experienced engineers to connect, collaborate and innovate. Being committed to education, the organization gives due importance to career growth and self-development. Science & Engineering graduates and working professionals will benefit directly from the organization activities. The organization advisory board of experienced and eminent engineers will offer technical and career guidance. And Dr. K.Durgaprasad is well known for his programming knowledge in P-Spice. In this field, he is having the experience of more than 15 years.

Report

1. Report in brief by Organizer / Coordinator / Convener: We all experienced engineering students who have difficulties with circuit fundamental principles, and will not benefit from a rigorous mathematical analysis. One solution is to use the teaching world of P-Spice simulation. And the main aim of this workshop is to educate the students about the importance of simulation tools. The workshop has been organized in two days. In the first day, the speaker elaborates about the importance of P-Spice, job opportunities available for the students who are good in P-Spice etc., and the explanation related to writing the program has been given. In the second day, morning session completely spent on writing programs for different electronic circuits, how to simulate the circuit etc., and in the afternoon session, hands on session has been conducted for the students in the simulation lab in the c-block. Students practice the programs with the guidance of the speaker and the faculty members.

2. Photos:





3. Feedback from students: Students' have got good knowledge in basic instructions of P-Spice. Interaction of the mentor is quite good with the students.

4. Remarks from Resource Person: Very good opportunities are available for the students who are good in programming by P-Spice. Students' are good and pay complete attention on my lecture. Hands on experience session is very much satisfactory. Faculty co-ordination is appreciable.