

M.TECH II SEMESTER (R16) REGULAR EXAMINATIONS – MAY/JUNE, 2017  
(2016 ADMITTED BATCH ONLY)

TIME TABLE

TIME: 10:00 AM TO 01:00 PM

BRANCHES AND SPECIALIZATIONS	22.05.2017 (Monday)	24.05.2017 (Wednesday)	26.05.2017 (Friday)	29.05.2017 (Monday)	31.05.2017 (Wednesday)	03.06.2017 (Friday)	05.06.2017 (Monday)	07.06.2017 (Wednesday)	09.06.2017 (Friday)	12.06.2017 (Monday)
<b>MECHANICAL MACHINE DESIGN/MECHANICAL ENGINEERING DESIGN (15/14)</b>	Experimental Stress Analysis (J1502)	Elective – IV Theory of Plasticity (J1510)	Finite Element Method (J2103)	Elective – III Computational Fluid Dynamics (J2104)	Design with advanced Materials (J1503)	Elective – IV Pressure Vessel Design (J1507) Mechanics of Composite Materials (J1508)	Optimization and Reliability (J1501)	Elective-III Signal Analysis & Condition Monitoring (J1505)	Elective – IV Mechatronics (J1509)	Elective – III Tribology (J1504) Design Synthesis (J1506)
<b>EEE Power Electronics, Power &amp; Industrial Drives, Power Electronics &amp; Electrical Drives, Power Electronics &amp; Power Electronics</b>	Switched Mode Power Conversion (J4301)	Custom Power Devices (J4304)	Digital Controllers (J4303)	Power Electronic Control of AC Drives (J4302)	Elective – III Renewable Energy System (J4305) Electrical Distribution Systems (J4307)	Elective – IV Smart Grid Technologies (J5605) Special Machines (J4308) Programmable Logic Controllers & Applications (J4309)	---	Elective – III Reactive Power Compensation & Management (J4306)	---	---
<b>ECE VLSI/VLSI DESIGN/VLSI SYSTEM DESIGN/VLSI &amp; MICRO ELECTONICS (57/72/61/76)</b>	Low Power VLSI Design (J6806)	CMOS Mixed Signal Circuit Design (J6802)	Elective – III VLSI Signal Processing (J6807)	Design for Testability (J6804)	Elective – IV System on Chip Design (J5702)	Elective – IV Optimization Techniques in VLSI Design (J5703) Semiconductor Memory Design & Testing (J6810)	Elective – III Digital Signal Processors & Architectures (J6805)	Elective – III CAD for VLSI (J5701)	Embedded System Design (J6801)	---
<b>CSE COMPUTER SCIENCE &amp; ENGINEERING (58)</b>	Advanced UNIX Programming (J4001)	Computer Networks (J5801)	Big Data Analytics (J4002)	Elective – I Compiler Design (J0504)	Elective – I Software Engineering (J0502) Artificial Intelligence (J0503)	Elective-II Image Processing (J0507) Parallel Algorithms (J0506) Cloud Computing (J2510) Mobile Computing (J5802)	Cyber Security (J2505)	---	Elective – I Machine Learning (J4006)	---

**NOTE:** i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY.  
ii) EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL.  
iii) THE PRINCIPALS ARE REQUESTED TO INFORM THE UNIVERSITY ANY OTHER SPECIALIZATIONS SUBJECTS THAT ARE NOT INCLUDED IN THE ABOVE TIME TABLE IMMEDIATELY.

Date: 27.04.2017

*MS/Exams/15m/FB/NS*  
*[Signature]*

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Controller of Examination

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA EXAMINATIONS BRANCH, KAKINADA- 533003**  
**M.TECH II SEMESTER (R13) SUPPLEMENTARY EXAMINATIONS – MAY/JUNE, 2017 (2013, 2014, & 2015 ADMITTED BATCHES ONLY)**

**TIME TABLE**

**TIME: 10:00 AM TO 01:00 PM**

BRANCHES AND SPECIALIZATIONS	22.05.2017 (Monday)	24.05.2017 (Wednesday)	26.05.2017 (Friday)	29.05.2017 (Monday)	31.05.2017 (Wednesday)	02.06.2017 (Friday)	05.06.2017 (Monday)	07.06.2017 (Wednesday)
<b>MECHANICAL</b> MACHINE DESIGN/MECHANICAL ENGINEERING DESIGN (15/14)	Experimental Stress Analysis (H1502)	---	Finite Element Method (H2103)	<b>Elective – III</b> Tribology (H1507) Signal Analysis & Condition Monitoring (H1508) Computational Fluid Dynamics (H2104)	<b>Elective – II</b> Fracture Mechanics (H1503) Gear Engineering (H1504) Design for Manufacturing	<b>Elective – IV</b> Pressure Vessel Design (H1510) Mechanics of Composite Materials (H1511) Mechanics (H1512)	Optimization and Reliability (H1501)	
<b>EEE</b> Power Electronics, Power & Industrial Drives, Power Electronics & Electrical Drives, Power Electronics & Drives, Electrical	Switched Mode Power Conversion (H4301)	Custom Power Devices (H4304)	Digital Controllers (H4303)	Electric Drives – II (H4302)	<b>Elective – III</b> Renewable Energy System (H4305) Electrical Distribution Systems (H4307)	<b>Elective – IV</b> Smart Grid (H5605) Special Machines (H4308) Programmable Logic Controllers & Applications (H4309)	---	<b>Elective – III</b> Reactive Power Compensation & Management (H4306)
<b>ECE</b> VLSI/VLSI DESIGN/VLSI SYSTEM DESIGN/VLSI & MICRO ELECTRONICS (57/72/61/76)	Low Power VLSI Design (H6809)	CMOS Mixed Signal Circuit Design (H6802)	<b>Elective – III</b> Scripting Language (H5702) VLSI Signal Processing (H6807)	Design for Testability (H6804)	<b>Elective – IV</b> System on Chip Design (E6806)	<b>Elective – IV</b> Optimization Techniques in VLSI Design (H5703) Semiconductor Memory Design & Testing (H6810)	<b>Elective – III</b> Digital Signal Processors & Architectures (H6805)	CAD for VLSI (H5701)
<b>CSE</b> COMPUTER SCIENCE & ENGINEERING (58)	<b>Elective-III</b> Advanced UNIX Programming (H4001)	Data Warehousing & Data Mining (H0501)	<b>Elective-II</b> Image Processing (H0508) Object Oriented Analysis and Design (H5804)	<b>Elective – I</b> Mobile Computing (H5802) Compiler Design (H5803) Human Computer Interaction (H0507)	Computer Networks (H5861)	<b>Elective-III</b> BIO Informatics (H5815) <b>Elective-III</b> Cloud Computing (H2508)	<b>Elective-II</b> Soft Computing H2512	Information Security (H4002)

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Date: 27.04.2017

MS/Exam/25 / 11/17/FS/MS

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*J. K. Reddy*

Controller of Examination